## IN THE SPECIFICATION:

The specification as amended below with replacement paragraphs shows added text with <u>underlining</u> and deleted text with <u>strikethrough</u>.

Please REPLACE the paragraph beginning at page 8, line 20, with the following paragraph:

Fig. <u>45-5</u> is a flow chart of the VLIW verification step S13. The VLIW verification step S13 includes an instruction slot configuration verification step S13-1 and a register conflict verification step S13-2.